

## Reliability and Test Condition

Item	Performance	Test Condition
<b>Electrical Performance Test</b>		
Inductance	Refer to standard electrical characteristics list	HP4284A,CH11025,CH3302,CH1320,CH1320S LCR Meter
DCR		CH16502,Agilent33420A Micro-Ohm Meter
Saturation Current (Isat)	Approximately $\Delta L30\%$ .	Saturation DC Current (Isat) will cause L0 to drop $\Delta L(\%)$
Heat Rated Current (Irms)	Approximately $\Delta T40^{\circ}\text{C}$	Heat Rated Current (Irms) will cause the coil temperature rise $\Delta T(^{\circ}\text{C})$ 1.Applied the allowed DC current 2.Temperature measured by digital surface thermometer
Operating Temperature	-40 $^{\circ}\text{C}$ ~+125 $^{\circ}\text{C}$ (Including self - temperature rise)	
Storage Temperature	1.-10~+40 $^{\circ}\text{C}$ ,50~60% RH (Product without taping) 2.-40~+125 $^{\circ}\text{C}$ (on board)	
<b>Reliability Test</b>		
Life Test	Appearance : No damage. Inductance : within $\pm 10\%$ of initial value Q : Shall not exceed the specification value RDC : within $\pm 15\%$ of initial value and shall not exceed the specification value	Preconditioning: Run through IR reflow for 2 times. ( IPC/JEDEC J-STD-020D Classification Reflow Profiles) Temperature : 125 $\pm 2^{\circ}\text{C}$ (Inductor) Applied current : rated current Duration : 1000 $\pm 12$ hrs Measured at room temperature after placing for 24 $\pm 2$ hrs
Load Humidity		Preconditioning: Run through IR reflow for 2 times. ( IPC/JEDEC J-STD-020D Classification Reflow Profiles) Humidity : 85 $\pm 2\%$ R.H Temperature : 85 $^{\circ}\text{C} \pm 2^{\circ}\text{C}$ Duration : 1000hrs Min. with 100% rated current Measured at room temperature after placing for 24 $\pm 2$ hrs
Moisture Resistance		Preconditioning: Run through IR reflow for 2 times. ( IPC/JEDEC J-STD-020D Classification Reflow Profiles) 1. Baked at50 $^{\circ}\text{C}$ for 25hrs, measured at room temperature after placing for 4 hrs. 2. Raise temperature to 65 $\pm 2^{\circ}\text{C}$ 90-100%RH in 2.5hrs, and keep 3 hours, cool down to 25 $^{\circ}\text{C}$ in 2.5hrs. 3. Raise temperature to 65 $\pm 2^{\circ}\text{C}$ 90-100%RH in 2.5hrs, and keep 3 hours, cool down to 25 $^{\circ}\text{C}$ in 2.5hrs, keep at 25 $^{\circ}\text{C}$ for 2 hrs then keep at -10 $^{\circ}\text{C}$ for 3 hrs 4. Keep at 25 $^{\circ}\text{C}$ 80-100%RH for 15min and vibrate at the frequency of 10 to 55 Hz to 10 Hz, measure at room temperature after placing for 1~2 hrs.
Thermal shock		Preconditioning: Run through IR reflow for 2 times. ( IPC/JEDEC J-STD-020D Classification Reflow Profiles) Condition for 1 cycle Step1 : -40 $\pm 2^{\circ}\text{C}$ 30 $\pm 5$ min Step2 : 25 $\pm 2^{\circ}\text{C}$ $\leq 0.5$ min Step3 : 125 $\pm 2^{\circ}\text{C}$ 30 $\pm 5$ min Number of cycles : 500 Measured at room temperature after placing for 24 $\pm 2$ hrs
Vibration		Oscillation Frequency: 10~2K~10Hz for 20 minutes Equipment : Vibration checker Total Amplitude:1.52mm $\pm 10\%$ Testing Time : 12 hours(20 minutes, 12 cycles each of 3 orientations)

## Reliability and Test Condition

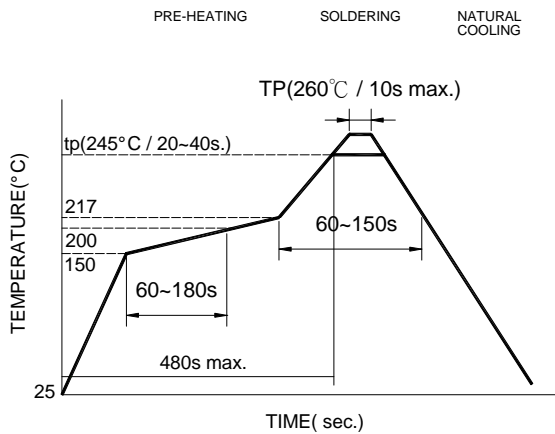
Item	Performance	Test Condition															
<b>Reliability Test</b>																	
Shock	Appearance : No damage. Inductance : within $\pm 10\%$ of initial value Q : Shall not exceed the specification value RDC : within $\pm 15\%$ of initial value and shall not exceed the specification value	<table border="1"> <thead> <tr> <th>Type</th> <th>Peak value (g's)</th> <th>Normal duration (D) (ms)</th> <th>Wave form</th> <th>Velocity change (Vi)ft/sec</th> </tr> </thead> <tbody> <tr> <td>SMD</td> <td>50</td> <td>11</td> <td>Half-sine</td> <td>11.3</td> </tr> <tr> <td>Lead</td> <td>50</td> <td>11</td> <td>Half-sine</td> <td>11.3</td> </tr> </tbody> </table>	Type	Peak value (g's)	Normal duration (D) (ms)	Wave form	Velocity change (Vi)ft/sec	SMD	50	11	Half-sine	11.3	Lead	50	11	Half-sine	11.3
Type		Peak value (g's)	Normal duration (D) (ms)	Wave form	Velocity change (Vi)ft/sec												
SMD	50	11	Half-sine	11.3													
Lead	50	11	Half-sine	11.3													
Bending		Shall be mounted on a FR4 substrate of the following dimensions: $\geq 0805$ inch(2012mm):40x100x1.2mm $< 0805$ inch(2012mm):40x100x0.8mm Bending depth: $\geq 0805$ inch(2012mm):1.2mm $< 0805$ inch(2012mm):0.8mm duration of 10 sec.															
Soderability	More than 95% of the terminal electrode should be covered with solder	Preheat: 150°C,60sec Solder: Sn96.5% Ag3% Cu0.5% Temperature: 245 $\pm$ 5°C Flux for lead free: Rosin. 9.5% Dip time: 4 $\pm$ 1sec Depth: completely cover the termination															
Resistance to Soldering Heat		Depth: completely cover the termination <table border="1"> <thead> <tr> <th>Temperature (°C)</th> <th>Time (s)</th> <th>Temperature ramp/immersion and emersion rate</th> <th>Number of heat cycles</th> </tr> </thead> <tbody> <tr> <td>260 <math>\pm</math>5 (solder temp)</td> <td>10 <math>\pm</math>1</td> <td>25mm/s <math>\pm</math>6 mm/s</td> <td>1</td> </tr> </tbody> </table>	Temperature (°C)	Time (s)	Temperature ramp/immersion and emersion rate	Number of heat cycles	260 $\pm$ 5 (solder temp)	10 $\pm$ 1	25mm/s $\pm$ 6 mm/s	1							
Temperature (°C)	Time (s)	Temperature ramp/immersion and emersion rate	Number of heat cycles														
260 $\pm$ 5 (solder temp)	10 $\pm$ 1	25mm/s $\pm$ 6 mm/s	1														
Terminal Strength	Appearance : No damage. Inductance : within $\pm 10\%$ of initial value Q : Shall not exceed the specification value RDC : within $\pm 15\%$ of initial value and shall not exceed the specification value	Preconditioning:Run through IR reflow for 2 times ( IPC/JEDEC J-STD-020D Classification Reflow Profiles) With the component mounted on a PCB with the device to be tested, apply a force ( $> 0805$ :1kg , $\leq 0805$ :0.5kg)to the side of a device being tested. This force shall be applied for 60 +1 seconds. Also the force shall be applied gradually as not to apply a shock to the component being tested. 															

Note : When there are questions concerning measurement result measurement shall be made after 48  $\pm$  2 hours Of recovery under the standard condition.

## Reliability and Test Condition

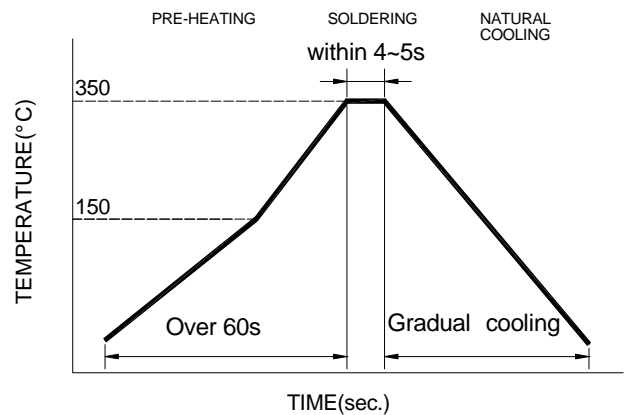
Item	Performance	Test Condition
<b>Soldering and Mounting</b>		
Soldering	Mildly activated rosin fluxes are preferred. The minimum amount of solder can lead to damage from the stresses caused by the difference in coefficients of expansion between solder, chip and substrate. JANTEK terminations are suitable for re-flow soldering systems. If hand soldering cannot be avoided, the preferred technique is the utilization of hot air soldering tools.	
Solder re-flow:	Recommended temperature profiles for re-flow soldering in Figure 1.	
Soldering Iron:	Products attachment with a soldering iron is discouraged due to the inherent process control limitations. If a soldering iron must be employed the following precautions are recommended. for Iron Soldering in Figure 2. Note : <ul style="list-style-type: none"> <li>• Preheat circuit and products to 150°C</li> <li>• Never contact the ceramic with the iron tip</li> <li>• Use a 20 watt soldering iron with tip diameter of 1.0mm</li> <li>• 355°C tip temperature (max)</li> <li>• 1.0mm tip diameter (max)</li> <li>• Limit soldering time to 4~5 sec</li> </ul>	

Reflow Soldering



Reflow times: 3 times max  
Fig.1

Iron Soldering



Iron Soldering times : 1 times max  
Fig.2